

PROPLUS ADVANCED DEVICE MODELING SEMINAR (PROPLUS 先進器件建模技術研討會)

DATE: SEP 25, 2008

日期: 2008年9月25日

PLACE: SHANGHAI PUDONG HOLIDAY INN HOTEL

地點: 上海浦東假日酒店 (上海浦東區東方路899號)

Agenda:

- 09:30-10:00: Registration
- 10:00-10:15: Opening Speech (Mr. Bob Xu, COO, ProPlus Design Solutions, Inc.)
- 10:15-11:00: Industry Trend and Challenges – ProPlus Modeling Technology Innovations (Dr. James Ma, CEO, ProPlus Design Solutions, Inc.)
- 11:00-12:00: Guest Keynote – Meeting the Modeling & Reliability Challenges of 45nm & Beyond (Dr. Zhihong Liu, Corporate VP, Cadence Design Systems, Inc.)
- 12:00-13:15: Lunch
- 13:15-14:00: Next-generation Device Modeling Solutions: PSP, HiSIM2 and BSIM-MG (Dr. Lianfeng Yang, GM, ProPlus Design Solutions, China)
- 14:00-14:45: High-voltage Modeling Approaches with BSIMProPlus (Dr. James Ma, CEO, ProPlus Design Solutions, Inc.)
- 14:45-15:15: Low-frequency Noise Modeling Applications Using NoisePro (Dr. Lianfeng Yang, GM, ProPlus Design Solutions, China)
- 15:15-15:30: Tea Break
- 15:30-15:45: Application of BSIMProPlus in Global Scaling MOSVAR Model with Temperature Effect (Mr. Leo Chen, SMIC)
- 15:45-16:00: Application of BSIMProPlus in Global Scaling Inductor Models with Temperature Effect (Mr. Danmy He, SMIC)
- 16:00-16:30: MOSFETs HCI/NBTI Reliability Modeling Practices (Dr. James Ma, CEO, ProPlus Design Solutions, Inc.)
- 16:30-16:45: ProPlus Products New Features Updates (Mr. Bob Xu, COO, ProPlus Design Solutions Inc.)
- 16:45-17:00: Q&A, Lucky Drawing